

2815

PATENT  
5500-36101/TT2823CPA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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9/15/4  
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Ok  
Lorenz  
T-7

Application No.: 09/207,972

Filed: December 9, 1998

Inventors:

Mark I. Gardner et al.

Examiner: Warren, Matthew E.

Group/Art Unit: 2815

Atty. Dkt. No: 5500-36101

Title: Ultrathin High-K Gate  
Dielectric with Favorable  
Interface Properties for  
Improved Semiconductor  
Device Performance

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

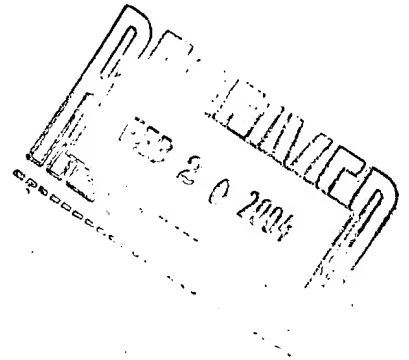
Printed Name

*[Signature]*  
Signature

February 4, 2004  
Date

RESPONSE TO OFFICE ACTION OF  
JANUARY 12, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Dear Sir:

This paper is submitted in response to the Office Action of January 12, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.